#### FIG. 1 PRIOR ART

1	c:\collections
2	notes.txt
3	myletter.doc
4	c-myhomepage
5	
6	s
7	homepage.html
8	myphoto.jpg

## FIG. 2

	1	c:\collections
	2	notes.txt
	3	myletter.doc
!-		
:	4	c-myhomepage
į	5	cspec
1	6	s
-	7	homepage.html
-	8	myphoto.jpg
Ĺ.		

## FIG. 3

1 collection c-myhomepage
2 coll-type cf-web-page
3 coll-desc A sample homepage collection
4 end-collection

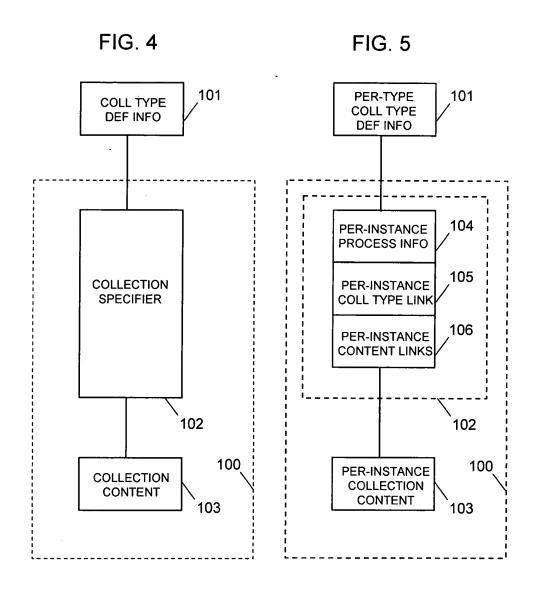


FIG. 6

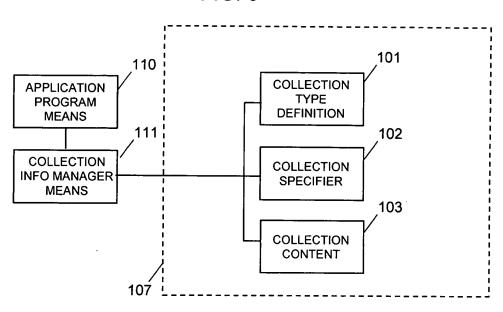
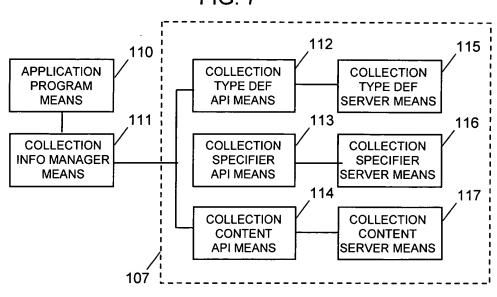


FIG. 7



#### FIG. 8

```
/* collection data structure */
 2 collection-info {
 3
      + specifier_info
 4
          + coll-type-indicator
 5
          + other specifier information ...
 6
      + content info
         + content_location_info ...
 7
 8
          + content_members ...
          + other content information...
      + other collection structure information...
10
11 }
```

#### FIG. 9

1 /\* collection type definition data structure \*/

```
collection-type-definition-info {
+ coll-type-name
+ collection internal structure info ...
+ collection content location info ...
+ collection content type recognition info ...
+ other collection type definition information...
}
```

## FIG. 10

#### <u>KEY</u>

### <u>VALUE</u>

	/* collection type interdir_source_files dir_doc_files	nal structure definitions */ ./s ./doc
	/* content location def	initions (per-type content links) */ http://host.com/some/dir/name
	content_subtree_ftp	
7	content_subtree_nfs	•
8	/* content type recogn	ition definitions */
9	content_policy	subtree_below_cspec_file
	content_file_type	.c file_cpp
	content_file_type	.c file_c
		.h file_c_include
		.doc file_ms_word
	content_file_type	
15	content_file_type	.xls file_ms_excel
	/* collection processing	g definitions */
17		yes
18	compiler_windows	vc++
19	<del>-</del>	gcc
20	•	Win98, Win2000, gnulinux
	process files	compile link
22	link libraries	stdio math sock
	/* results dispatching of	
24	results_ftp_host	ftp.output.com
25	results_ftp_dir	c:\ftphome\collection\results



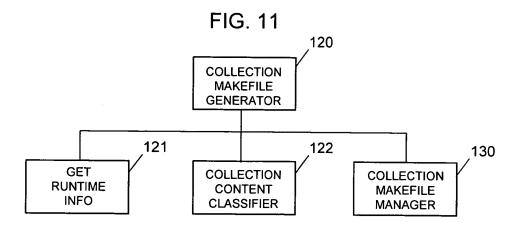


FIG. 12

- 1 /\* simplified algorithm for collection makefile generator \*/
- 2 Call get runtime info to get invocation parameters
- 3 Call collection content classifier to classify collection content
- 4 Call collection makefile generator manager to generate a complete makefile, passing classifier information as input

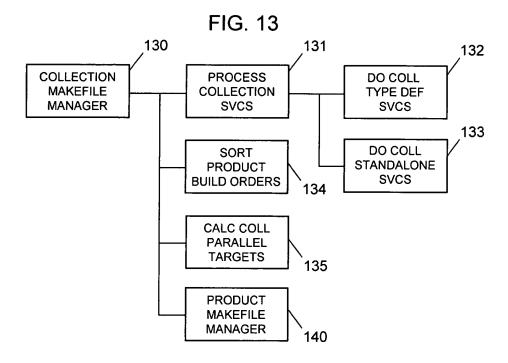


FIG. 14

- 1 /\* simplified algorithm for collection makefile manager \*/
- 2 Process collection-level fragments
- 3 Process fragments from collection type definition
- 4 Process fragments from collection specifier
- 5 Determine relative build order among multiple products
- 6 Determine number, names of coll-level parallel build targets
- 7 Loop over each product in collection
- 8 Process each product by calling product makefile manager

FIG. 15

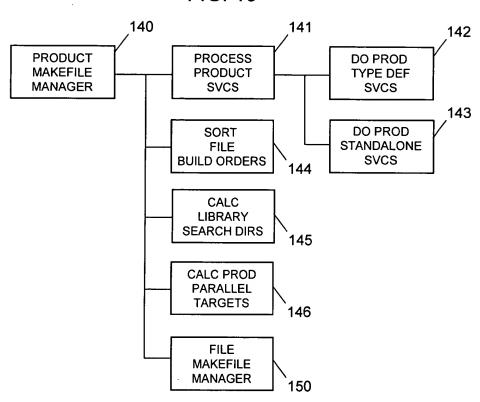


FIG. 16

- 1 /\* simplified algorithm for processing one product \*/
- 2 Process product-level fragments
- 3 Process fragments from product type definition
- 4 Process fragments from product section of collection specifier
- 5 Determine relative build order among content files for product
- 6 Determine number, names of product-level parallel build targets
- 7 Loop over each content file
- 8 Process each content file by calling file makefile manager

FIG. 17 150 151 CALC INCLUDE FILE MAKEFILE MANAGER **SEARCH DIRS** 152 DO FILE TYPE DEF **SVCS** 153 DO ACTION TYPE DEFI **SVCS** 

FIG. 18

- 1 /\* simplified algorithm for processing one content file \*/
- 2 Calculate include file search directories
- 3 Process fragments from content type definition
- 4 Process fragments from action type definition

FIG. 19

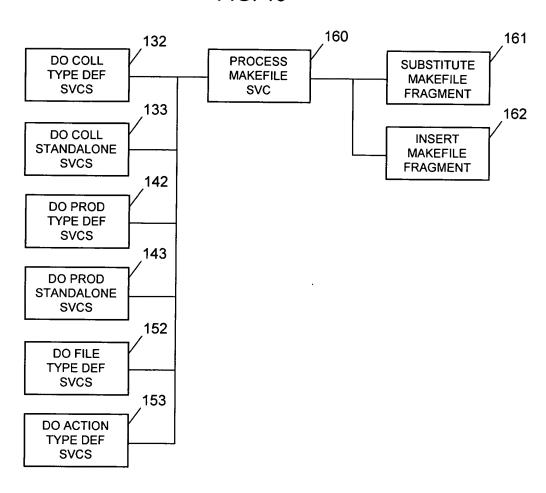


FIG. 20

- 1 /\* simplified algorithm for processing one fragment \*/
- 2 Substitute replacement values for placeholder strings
- 3 Insert substituted fragment into makefile data structure

+--

#### FIG. 21

```
c:\collections
 1
 2
        c-my-example
 3
            cspec
 4
           s
 5
               pi
 6
                 cmdline.h
 7
              win98
 8
                 cmdline.c
 9
              gnulinux2
10
                 cmdline.c
11
           lib
12
              pi
13
                 libfuns.h
14
                 libfuns.c
```

1

cspec:

```
2
     collection c-my-example
 3
     coll-type
                 ct-program
     coll-desc A multi-platform C program with library.
 4
 5
     end-collection
 6
     product
                 myprog
 7
     prod-type
                pt-program
 8
     prod-desc A program product.
                team-lib gnulinux-lib
 9
     libs
10
     end-product
11
     product
                 mylibrary
12
     prod-type
                pt-library
     prod-desc A library product.
13
14
     end-product
```

1 2 3 4	/* classification outp collection coll-type other coll classifi	out for gnulinux2 platform */ c-my-example ct-program cation info
5 6 7	/* classification info product prod-type	for a program product */ myprog pt-program
8 9 10 11 12	content content-path content-type content-language end-content	cmdline.h /s/pi/cmdline.h ctype-c-header c
13 14 15 16 17 18 19 20 21	content content-path content-type content-language content-dep content-dep content-dep content-dep end-content	cmdline.c/s/gnulinux2/cmdline.c ctype-c-source c/s/pi/cmdline.h/lib/pi/libfuns.h external-incl-file.h team-incl.h
22	end-product	

1	/* classification out	put */
2	collection	c-my-example
3	/* classification	info for the host collection */
4	/* classification	info for the program product */
5	/* classification info	for a library product */
6	product	mylibrary
7	prod-type	pt-library
10	content	libfuns.h
11	content-path	/lib/pi/libfuns.h
12	content-type	ctype-c-header
13	content-language	c
15	end-content	
16	content	libfuns.c
17	content-path	/lib/pi/cmdline.c
18	content-type	ctype-c-source
19	content-language	c
21	content-dep	/lib/pi/libfuns.h
22	end-content	•
23	end-product	

1 2 3 4	collection type definition product type definition content type definition action type definition	n information on information
5 6	cspec: coll-type	ct-program
7 8 9	index-coll-types.tbl: ct-program ct-web-page	ct-program.def ct-web-page.def
10 11	ct-program.def: product-type-index	index-product-types.tbl
12 13	index-product-types.tbl: pt-program	pt-program.def
14 15	pt-program.def: content-type-index	index-content-types.tbl
16 17	index-content-types.tbl ctype-c-source	content-c.def
18 19	content-c.def: action-type-index	index-action-types.tbl
20 21	index-action-types.def: action-c-source	action-c-source.def
22 23	action-c-source.def: action definition inform	mation

1 2 3 4	index-coll-types.tbl: ct-program ct-library ct-doc-html	ct-program.def ct-library.def ct-html.def
5 6 7	ct-program.def: /* type definition info for a " product-type-index	ct-program" collection type */ index-prod-program.tbl
8	base-template	base-template.tpl
9 10 11 12 13 14 15	service service service service service service service	svc-coll-macro-platform svc-coll-macro-site svc-coll-macro-tool-names svc-coll-macro-compiler svc-coll-macro-suffix svc-coll-target-defaults svc-coll-target-others
16	parallelism-max	4
17	other collection type info	)

### FIG. 27

1	index-prod-program.tbl:	
2	pt-program	pt-program.def
3	pt-program-java	pt-program-java.def
4	pt-program-unix	pt-program-unix.def
5	pt-program-win	pt-program-win.def

6 pt-program.def:

7 /\* type definition info for a "program" product type \*/

8 dir-source-files dirs-source.lst 9 dir-library-files dirs-library.lst

10 dir-include-files dirs-include.lst

11 file-identification-table file-identification.tbl

12 content-type-index index-content-types.tbl

13 service svc-prod-program

14 ... other product type info

## FIG. 28

1	index-content-types.tbl:	
2	ctype-c-source	content-c.def
3	ctype-c-header	content-c-h.def
4	ctype-csh	content-csh.def
5	ctype-html	content-html.def
6	content-c.def:	
7	/* type definition info for a	"c" file type */
8	type	c-source
9	language	С
0	action	action-c-source
1	action-type-index	index-action-types.tbl
2	service	svc-file-c-source
3	other content type defin	ition info

1	index-action-types.tbi:	
2	action-c-source	action-c-source.def
3	action-c-header	action-c-header.def
4	action-csh	action-csh.def
5	action-html	action-html.def
6	action-c-source.def:	
7	parser-type	internal
8	parser-name	internal-c
9	service	svc-action-c-source

## FIG. 30

1 idx-makefile-services.tbl:

2	/* services for collections */	1
3	svc-coll-macro-platform	coll-macro-platform.tpl
4	svc-coll-macro-site	coll-macro-site.tpl
5	svc-coll-macro-compiler	coll-macro-compiler.tpl
6	svc-coll-macro-toolnames	coll-macro-toolnames.tpl
7	svc-coll-macro-file-suffix	coll-macro-file-suffix.tpl
8	svc-coll-target-defaults	coll-target-defaults.tpl
9		
10	/* services for products */	
11	svc-prod-program	prod_prog_pi.tpl
12	svc-prod-program	prod_prog_os.tpl
13	svc-prod-program	prod_prog_pd.tpl
14		
15	svc-prod-library	prod-lib-pi.tpl
16	svc-prod-library	prod-lib-os.tpl
17	svc-prod-library	prod-lib-pd.tpl
18		
19	/* services for files */	
20	svc-file-c-source	file-c.tpl
21	svc-file-c-header	file-c-header.tpl
22	svc-file-f90	file-f90.tpl
23	svc-file-f90-header	file-f90-header.tpl
24	svc-file-f90-module	file-f90-module.tpl
25		
26	/* services for actions */	
27	svc-action-c-source	action-c-source.tpl
28	•••	
29	/* services for application to	asks */
30	svc-app-chmod	app-chmod.tpl
31	svc-app-copy-file	app-copy-file.tpl
32		

+

## FIG. 31

1	coll-macro-platform.tpl:
2	# This file defines platform-specific makefile macros
3	
4	fragment-begin
5	_marker_ marker-htree copy
6	# The holding area for shared files and libraries
7	HTREE=/site/h
8	fragment-end
9	
0	fragment-begin
1	_marker_ marker-macros1 copy
2	# makefile platform name, virtual platform name
3	MP=win98.plt
4	VP=win98
5	fragment-end

- 1	con-macro-site.tpi.
2	# This file defines site-specific makefile macros
3	fragment-begin
4	_marker_ marker-macros1 copy
5	
6	# places where shared files go
7	SHARE_DIR=\$(HTREE)\share
8	
9	# places where web pages go
10	HOST_WEB=www.your_domain.com
11	•••
12	fragment-end

#### FIG. 33

```
1
     coll-macro-toolnames.tpl:
 2
    # define macros for various program names
     fragment-begin
 4
     _marker_ marker-macros1 copy
 5
 6
    LS=Is
 7
     DIR=dir
 8
     RM=rm
9
     CP=cp
     ZIP=zip
10
     UNZIP=unzip
11
12
    CC=gcc
13
    LIB=Id
14
    RMDIR=rm
15
    fragment-end
```

```
coll-macro-compiler.tpl:
 1
 2
     # This file defines compiler options
 3
     fragment-begin
 4
     _marker_ marker-macros1 copy
5
6
     # default compiler options
7
     OPT=
8
     DEBUG=
9
     # default linker options
10
     LIBSPATH = $(HTREE)/$(MP)
11
     LDFLAGS= -s
12
     LPP= -L
13
14
     fragment-end
```

### FIG. 35

```
1
     coll-macro-suffix.tpl:
     # defines macros for file suffixes for this platform
 2
 3
     fragment-begin
     _marker_ marker-macros1 copy
 4
 5
 6
     # objects, executables, libraries, archives
 7
     O=.0
 8
     SO=.so
 9
     X=
10
     L=.a
11
     A=
12
     AWKS=.awk
     SEDS=.sed
13
     LEXS=.I
14
15
     YACS=.y
16
     CLASS=.class
17
     fragment-end
```

#### FIG. 36

```
1
     coll-target-defaults.tpl:
     # This file defines default makefile targets
 2
 3
     fragment-begin
 4
     _marker_ marker-targets0 copy
 5
 6
     # default targets used by all makefiles
 7
     default: build
 8
 9
     all: build exports
10
11
     build:
12
13
     exports:
14
     fragment-end
```

+

#### FIG. 37

/\* fragment commands \*/ fragment-begin / fragment-end 2 marker-name copy \_marker\_ 3 \_macro\_ macro-name append value1 value2... 4 \_target\_ add-deps dep1 dep2 ... target-name 5 \_target\_ target-name сору \_target\_ target-name copy-force

#### FIG. 38

base-template.tpl:
# marker-htree
# marker-macros1
# marker-targets0

```
makefile.out:
 1
 2
 3
     # The holding area for shared files and libraries
 4
     HTREE=/site/h
 5
     # marker-htree
 6
 7
     # makefile platform name, virtual platform name
 8
     MP=win98.plt
 9
     VP=win98
10
11
     # places where shared files go
12
     SHARE_DIR=$(HTREE)\share
13
14
     LS=Is
15
     DIR=dir
16
17
     OPT=
18
     DEBUG=
19
20
     O=.0
21
     SO=.so
22
     X=
23
24
     # marker-macros1
25
26
     # default targets used by all makefiles
     default: build
27
28
29
     all: build exports
30
31
     build:
32
33
     exports:
     # marker-targets0
34
```

#### FIG. 40

```
1
     prod-prog-pi.tpl:
 2
     # Define platform-independent macros for programs
 3
 4
     fragment-begin
 5
     _marker_marker-macros1 copy
     # Initialize these macros so they are defined.
 6
     ALL_OBJS__prod_=
 7
     OBJ_PI__prod_=
 8
 9
     OBJ_F90__prod_=
     OBJ_F90_MOD__prod_=
10
11
12
     # create one macro to hold all objects
     ALL_OBJS__prod_=$(OBJ_PI__prod_) \
13
        $(OBJ_F90__prod_) $(OBJ_F90_MOD_prod_)
14
15
16
     # add marker to anchor linker macro later
17
     # marker-link-cmd
18
     fragment-end
```

1	proa-prog-os.tpi:	
2	# Define operating system macros for programs	
3	•	
4	# Adds program name dependency to build targ	et.
5	fragment-begin	
6	_target_ build add_deps _mprod_\$(X)	
7	fragment-end	
8		
9	# Adds program name dependency to export tail	get
0	fragment-begin	
1	_target_ exports add_deps _mprod_\$(X)	
2	fragment-end	

```
1
     prod-prog-pd.tpl:
 2
     # Define platform-dependent macros for programs
 3
 4
     fragment-begin
 5
      _marker_ marker-macros1 copy
     # default compiler flags for this platform
 7
     CCFLAGS1= -Wall -ansi -pipe -I.
 8
     CCFLAGS2= -I- -c
 9
     fragment-end
10
11
     fragment-begin
12
     _marker_ marker-link-cmd copy
13
     # linker command for this platform
14
     LDLIBS=
     \label{local_prod} $$LD\_prod_=${CC} -o \_mprod\_\_lib\_dirs\_ \ $$
15
               $(ALL_OBJS__prod_) _lib_names_
16
17
     fragment-end
18
19
     fragment-begin
20
     # add link command to target for program product
21
     _target_ _mprod_$(X) copy
22
        $(LD__prod_) $(LDFLAGS)
23
        $(CHMOD) 775 _mprod_$(X)
24
     fragment-end
25
26
     fragment-begin
27
     # add object dependencies to product target
28
      target mprod $(X) add deps $(OBJ PI prod )
29
     fragment-end
```

1	_prod_	name of product from cspec
2	_mprod_	name of product file on disk
3	_ptype_	product type of current product
4	_src_file_path_	source file pathname
5	_src_file_name_	source file filename
6	_src_file_name_no_suf_	source filename with no suffix
7	_target_list_	list of makefile targets
8	_target_name_	name of current target
9	_deplist_	list of dependent targets
10	_incl_dirs_	list of include directories
11	_lib_dirs_	list of library directories
12	_lib_names_	list of library names
13	_zpln_	parallel target number 01,02,etc

#### FIG. 44

```
makefile.out:
 1
 2
 3
     # Initialize these macros so they are defined.
     ALL_OBJS_myprog=
 4
 5
     OBJ_PI_myprog=
     OBJ_F90_myprog=
 6
     OBJ_F90_MOD_myprog=
 7
 8
 9
     # create one macro to hold all objects
10
     ALL_OBJS_myprog=$(OBJ_PI_myprog) \
        $(OBJ_F90_myprog) $(OBJ_F90_MOD_myprog)
11
12
13
     # marker-link-cmd
14
15
     # marker-macros1
16
17
     # default targets used by all makefiles
     default: build
18
19
20
     all: build exports
21
22
     build: myprog
23
24
     exports: myprog
25
     # marker-targets0
```

+

```
1
     makefile.out:
 2
 3
     # Initialize these macros so they are defined.
 4
     ALL_OBJS_myprog=
 5
     OBJ_PI_myprog=
 6
 7
     # create one macro to hold all objects
     ALL_OBJS_myprog=$(OBJ_PI_myprog) ...
 8
 9
10
     # linker command for this platform
11
     LDLIBS=
12
     LD_myprog=${CC} -o myprog $(LDLIBS) \
13
              $(ALL_OBJS_myprog) $(lb)
14
     # marker-link-cmd
15
16
     # default compiler flags for this platform
17
     CCFLAGS1= -Wall -ansi -pipe -I.
18
     CCFLAGS2= -I- -c
19
     # marker-macros1
20
21
     build: myprog
22
23
     exports: myprog
24
25
     # add link command to target for program product
26
     myprog: $(OBJ_PI_myprog)
        $(LD_myprog) $(LDFLAGS)
27
28
        $(CHMOD) 775 myprog
     # marker-targets0
29
```

#### FIG. 46

```
1
     file-c-source.tpl:
 2
     # process files
 3
 4
     # add current source file to top src file macro
 5
     fragment-begin
     _macro_ SRC_C
 6
                               append _src_file_path_
 7
     fragment-end
 8
9
     # add current source file to product source file macro
10
     fragment-begin
     _macro_ SRC_C__prod_ append _src_file_path_
11
12
     fragment-end
```

```
1
     action-c-source.tpl:
 2
     # process files
 3
 4
     # add compilation command under C object targets.
 5
     fragment-begin
 6
     _target_ _target_name_$(O) copy
 7
        $(CC) $(OPT) $(DEBUG) $(CCFLAGS1) \
 8
           _incl_dirs_ $(CCFLAGS2) _src_file_path_
 9
     fragment-end
10
11
     # add dependency list to C object target.
12
     fragment-begin
13
     _target_ _target_name_$(O) add_deps _deplist_
14
     fragment-end
```

```
makefile.out:
 2
 3
      SRC_C= ../s/gnulinux2/cmdline.c ...
 4
      SRC_C_prod_= ../s/gnulinux2/cmdline.c ...
 5
 6
 7
      # default compiler flags for this platform
     CCFLAGS1= -Wall -ansi -pipe -I.
 8
 9
      CCFLAGS2= -I- -c
10
     # marker-macros1
11
     # default targets used by all makefiles
12
13
     default: build
14
15
     all: build exports
16
17
     build: myprog
18
     exports: myprog
19
20
     cmdline.o: ../s/pi/cmdline.h ../lib/pi/libfuns.h
21
22
        $(CC) $(OPT) $(DEBUG) $(CCFLAGS1) \
23
           _incl_dirs_ $(CCFLAGS2) ../s/gnulinux2/cmdline.c
24
25
     # marker-targets0
26
```

1 2 3 4 5	collection coll-type coll-desc svc end-collection	c-my-example ct-program A fileset example svc-coll-cleanup
6	product	myprog
7	prod-type	pt-program
8	libs	mylib
9	SVC	svc-app-copy-file myprog myprog.bak
10	end-product	

1 2	cspec:	
3 4 5 6	product prod-type prod-desc end-product	myprog pt-program A normal program binary executable.
7 8 9 10	product prod-type prod-desc replace-name end-product	myprog-2 pt-shared-object A shared object program executable myprog
12 13		comes cspec name myprog-so comes diskfile name myprog
14 15 16 17	_mprod_\$(X): \$(LDprod	mand to target for program product  d_) \$(LDFLAGS_prod_)  775 _mprod_\$(X)
18 19 20 21	myprog\$(X): \$(LD_mypro	product myprog  og) \$(LDFLAGS_myprog)  775 myprog\$(X)
22 23 24 25	myprog\$(SO): \$(LD_mypro	product myprog-so og-2) \$(LDFLAGS_myprog-2) 775 myprog\$(SO)

## FIG. 51

1	product-build-order.tbl:		
2	# define relative	build order among produ-	cts
3		•	
4	pt-initial	10	
5	pt-data	50	
6	pt-library	100	
7	pt-program	1000	
8	pt-script	1000	

```
makefile.out:
...
# dependent targets mylib and myprog appear in proper
# product build order, from left to right
#
build: mylib myprog
mylib:
...
myprog:
...
```

## FIG. 53

1	file-build-order.tbl:	
2	# define relative buil	d order among file types
3		
4	ft-resource	10
5	ft-precompiled-cpp	20
6	ft-c-source	50

```
makefile.out:
 1
 2
 3
4
      # dependent targets mylib and myprog appear in proper # product build order, from left to right
 5
      #
 6
      build: mylib myprog
 7
 8
      mylib:
 9
10
      myprog: myresource.rc myprecompiled-header.o cmdline.o
11
```

#### FIG. 55

7	airc		11140	104.
1	111112	<b></b>	lude.	1
•	OII C	, ,,,,	nuuç.	101.

2	dir/gnulinux2 dir/gnulinux2	/site/myteam/include/gnulinux2 /site/myteam/include/gnulinux
4	dir/gnulinux2	/site/include/gnulinux2
5	dir/gnulinux2	/site/include/gnulinux

- 1 # suppose these are paths to example include files
- 2 /site/include/gnulinux2/external-incl-file.h
- 3 /site/myteam/include/gnulinux/team-incl.h
- 4 # include files matched by search rules, in order
- 5 /site/myteam/include/gnulinux/team-incl.h
- 6 /site/include/gnulinux2/external-incl-file.h
- 7 \_incl\_dirs\_ = -I /site/myteam/include/gnulinux \ ... -I /site/include/inux2
- 8 makefile.out:
- 9 ..
- 10 file1.o: ../s/file1.c
- 11 \$(CC) \$(OPT) \$(DEBUG) \$(CCFLAGS1) \
- 12 -I /site/myteam/include/gnulinux -I /site/include/inux2 \
- 13 \$(CCFLAGS2) ../s/file1.c

#### FIG. 57

	•	
2	dir/gnulinux2	/site/myteam/lib/gnulinux2
3	dir/gnulinux2	/site/myteam/lib/gnulinux
4	dir/gnulinux2	/site/lib/gnulinux2
5	dir/gnulinux2	/site/lib/gnulinux

1

dirs-library.lst:

```
# suppose these are paths to example libraries
 1
 2
      /site/lib/gnulinux2/gnulinux-lib.a
 3
      /site/myteam/lib/gnulinux/team-lib.a
 4
     # libs matched by search rules, in order
      /site/myteam/lib/gnulinux/team-lib.a
 5
 6
      /site/lib/gnulinux2/gnulinux-lib.a
      _lib_dirs = -L /site/myteam/lib/gnulinux -L /site/lib/inux2
 7
     _lib_names_ = -l team-lib.a gnulinux-lib.a
 8
 9
      makefile.out:
10
     LD_mprog = $(LD) -L /site/myteam/lib/gnulinux \
11
12
               ... -L /site/lib/gnulinux2 \
13
               ... -l team-lib.a -l gnulinux-lib.a
14
15
     myprog$(X): ...
16
        $(LD_mprog) ...
```

## FIG. 59

1	virtual-platforr	n.tbl:			
2	#				
3	#	Specific	Generic	Family	Every
4	# Name	OS	os	os ´	os
5	#				
6	gnulinux2.plt	gnulinux2	gnulinux	unix	pi
7	sol28.plt	sol28	sol	unix	pi
8	win98.plt	win98	win9	win	pi
9	win95.plt	win95	win9	win	pi
10	winnt40.plt	winnt40	winnt	win	pi
11	win2000.plt	win2000	winnt	win	pi

- 1 # fragment search directories for win98 platform
- 2 fragments/win98
- 3 fragments/win9
- 4 fragments/win
- 5 fragments/pi
- 6 # fragment search directories for gnulinux 2 platform
- 7 fragments/gnulinux2
- 8 fragments/gnulinux
- 9 fragments/unix
- 10 fragments/pi

1 2 3 4	collection coll-type coll-desc end-collection	c-my-exampl ct-program A fileset exar	
5	product	myprog	
6	prod-type	pt-program	
7	libs/pi	mylib	r-gnulinux-lib
8	libs/gnulinux	mylib myothe	
9 10 11 12	svc/pi svc/gnulinux svc/win98 end-product	svc-prod-name svc-prod-name svc-prod-name	svc arguments svc args svc args

```
1
      makefile.out
 2
 3
      myprog: file-001.o file-002.o ... file-100.o
 4
         $(LD_mprog) ...
     # GNU make parallelism with -jobs argument will compile
 5
 6
     # 4 files at a time to build the myprog target
 7
 8
     make -j 4 myprog
 9
     # without a parallel make tool, makefile targets must be
10
     # generated to offer parallelism, as follows:
11
     #
12
     myprog: myprog-01 myprog-02 myprog-03 myprog-04
13
     myprog-01: file-001.o file-002.o ... file-025.o
14
     myprog-02: file-026.o file-027.o ... file-050.o
15
     myprog-03: file-051.o file-052.o ... file-075.o
16
     myprog-04: file-076.o file-077.o ... file-100.o
17
     # now parallel commands can be issued against parallel targets
18
     # running on multiple machines
19
     on machine1: make myprog-01
20
     on machine2: make myprog-02
21
22
     # running multiple windows on one machine
23
     in shell window 1: make myprog-01
24
     in shell window 2: make myprog-02
25
26
     # or running in the background on one machine
27
     in shell window 1: make myprog-01 &
28
     in shell window 1: make myprog-02 &
29
```

#### FIG. 63

```
1
     action-c-source.tpl:
 2
     # process files
 3
 4
     # this line adds the parallelism-specific object file macro to the
 5
     # "master" or "top level" object file macro.
 6
     fragment-begin
 7
     _macro_ OBJ_Pl__prod_ append $(OBJ_Pl__prod__zpln_)
 8
     fragment-end
 9
10
     # this line adds current object file to correct
     # parallelism-specific object file macro
11
12
     fragment-begin
13
     _macro_ OBJ_Pl__prod___zpln_ append _target_name_$(O)
14
     fragment-end
15
     # this line adds the parallelism-specific object file macro as a
16
17
     # dependency of the parallelism-specific build target.
18
     fragment-begin
19
     target_build_zpln_add_deps $(OBJ_PI_prod_zpln_)
20
     fragment-end
```

```
makefile.out:
 1
 2
 3
                         = file-001.o file-002.o ... file-100.o
     OBJ_PI_myprog
 4
     OBJ_PI_myprog 01 = file-001.o file-002.o ... file-025.o
 5
     OBJ_PI_myprog_02 = file-026.o file-027.o ... file-050.o
 6
7
     build_01: $(OBJ_Pl_myprog_01)
 8
9
     build_02: $(OBJ_PI_myprog_02)
10
```

#### FIG. 65

```
makefile.out:
 2
     # sequential and parallel targets for multiple products
 3
 4
     # target for building all products sequentially
 5
     build: build_01 build_02 build_03
 6
 7
     # parallel targets for building all products in parallel
 8
     build_01: myprog-01 product2-01 product3-01 ...
 9
     build_02: myprog-02 product2-02 product3-02 ...
10
11
     # target for building product 'myprog' sequentially
12
     myprog: myprog-01 myprog-02 myprog-03
13
14
     # parallel targets for building product 'myprog' in parallel
     myprog-01: $(OBJ_PI_myprog_01)
15
     myprog-02: $(OBJ_PI_myprog_02)
16
17
18
     # target for building product 'product2' sequentially
     product2: product2-01 product2-02 ...
19
20
     # parallel targets for building product 'product2' in parallel
21
22
     product2-01: $(OBJ_PI_product2_01)
     product2-02: $(OBJ_PI_product2_02)
23
24
```

+-